

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

MAY 10 2005

(Use several sheets if necessary)

Applicant

KAMEYAMA, et al.

Filing Date

July 22, 2004

Group Art Unit

2819

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
UR	5,930,161	07/27/99	Sheikholeslami, et al.			
UR	5,821,578	10/13/98	Noriyuki SHIMOJI			
UR	5,982,683	11/09/99	WATSON, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER



DATE CONSIDERED

8/24/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

**OPTIONAL INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

AUG 25 2004

(Use several sheets if necessary)

Applicant

KAMEYAMA, et al.

Filing Date

July 22, 2004

Group Art Unit

Not Assigned


U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	KIMURA, et al., "Ferroelectric-Based Functional Pass-Gate for Low-Power VLSI", 2002 Symposium On VLSI Circuits Digest of Technical Papers, pages 196-199, Japan	No month

EXAMINER



DATE CONSIDERED

8/24/04

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